

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: CHANG, Yi-Yang

SERIAL NO.:

FILED: Herewith

TITLE: SIGNAL DETECTOR

PRELIMINARY AMENDMENT

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In conjunction with the filing of the present application, and prior to an initial Official Action on this matter, please amend the above-identified application as follows:

Preliminary Amendment: SPECIFICATION AMENDMENTS

In Paragraph [0007], please amend the paragraph as follows:

3. The main functions of the clock signal detector 10 and the data signal detector 20 are designed to instantly detect interruptions in the clock signal transmission or abnormal transmission in data signals. Coordinated with an interruption control circuit, the system is able to prevent continuous laser emission and in turn prevent the laser emitter from ~~sever~~ severe exhaustion or breakdown.

In Paragraphs [0010] to [0017], please amend the paragraphs as follows:

[0010] FIG. 1 shows a schematic view of the external wiring of the invention.

[0011] FIG. 2 shows a schematic view of the internal wiring of the invention.

[0012] FIG. 3 shows a schematic view of the internal wiring of the clock signal detector = as an implemented example.

[0013] FIG. 4 shows a schematic view of the signal conversion procedures of the clock signal detector = as an implemented example.

[0014] FIG. 5 shows a schematic view of the internal wiring of the data signal detector= as an implemented example.

[0015] FIG. 6 shows a schematic view of the signal conversion procedures of the data signal detector = as an implemented example.

[0016] FIG. 7 shows a schematic view of the internal wiring of the integral charger in the data signal detector= as an implemented example.

[0017] FIG. 8 shows a schematic view of the integral signal detection procedures in the integral charger of the data signal detector= as an implemented example.